

ABSTRACT

This invention is a method comprising providing a substrate, forming a first layer on the substrate, wherein the first layer has a dielectric constant of less than 3.0 and comprises an organic polymer, applying an organosilicate resin over the first layer,
5 removing a portion of the organosilicate resin to expose a portion of the first layer, and removing the exposed portions of the first layer. The invention is also an integrated circuit article comprising an active substrate containing transistors and an electrical interconnect structure containing a pattern of metal lines separated, at least partially, by layers or regions of an organic polymeric material having a dielectric constant of less than 3.0 and
10 further comprising a layer of an organosilicate resin above at least one layer of the organic polymer material.

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